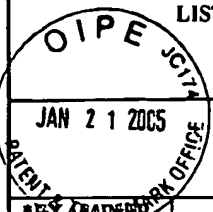


FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO.: 57941.000063		SERIAL NUMBER: 10716,596	
<div style="text-align: center;"> LIST OF MATERIALS CITED BY APPLICANT (Use several sheets if necessary) Page 1 of 1 </div>				APPLICANT(S): MICHAEL FARMWALD et al.		GROUP ART UNIT: 2818 211	
				FILING DATE: November 20, 2003			
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		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
GA	1.	5742798	04-21-1998	Goldrian			
GA	2.	4480307	10-30-1984	Budde et al.			
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						Yes	No
GA	3.	2000035831 A	02-02-2000	JP		X (Abstract)	
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OTHER MATERIALS (Including Author, Title, Date, Pertinent Pages, Etc.)							
GA	5.	Opposition against European patent 0 525 068, (Application No. 91 908 374.1-2201) (German & English Translation), 04-15-2004					
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	7.	Brief Communication in the Opposition against European patent 1 022 642 (Application No. 00108822.8), 04-16-2004					
	8.	Minutes of the oral proceedings of February 10, 2004 in the Opposition against European patent 0 525 068, 02-18-2004					
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	11.	Opposition against European patent 1004956, (Application No. 00010832.4) (German & English Translation), 03-03-2004					
	12.	Opposition against European patent 1022642, (Application No. 0010822.8) (German & English Translation), 07-15-2004					
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					FILING DATE: November 20, 2003		GROUP: 2818 2 111	

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				Yes	No		
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GA 25.	63217452	09/09/1988	JP				
GA 26.	63239676	10/05/1988	JP				
GA 27.	6443894	02/16/1989	JP				

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number 10/716,596
Filing Date November 20, 2003
First Named Inventor Michael Farnwald
Art Unit ~~2818~~ 2111
Examiner Name Unassigned Auve
Attorney Docket Number 57941.000063

Sheet 1 of 1

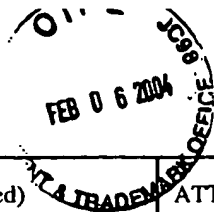
OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
GA		Invalidity argument materials of potential licensee relating to claim 1 of U.S. Patent No. 6,034,918, which is related to present U.S. Patent Application No. 10/716,596 by virtue of the fact that both claim priority to U.S. Patent Application No. 07/510,898 (now abandoned), citing J.L. Rodengen, The Legend of Amdahl, Write Stuff Syndicate, Inc., 2000, and FACOM M-780, Operating Manual, including two separate English language translations thereof.	<input checked="" type="checkbox"/>	<input type="checkbox"/>
GA		Invalidity argument materials of potential licensee relating to claim 23 of U.S. Patent No. 6,038,195, to which present U.S. Patent Application No. 10/716,596 claims priority and is related by virtue of the fact that both claim priority to U.S. Patent Application No. 07/510,898 (now abandoned), citing J.L. Rodengen, The Legend of Amdahl, Write Stuff Syndicate, Inc., 2000, and FACOM M-780, Operating Manual, including two separate English language translations thereof.	<input checked="" type="checkbox"/>	<input type="checkbox"/>
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EXAMINER SIGNATURE /Glenn Auve/

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	APPLICANT(S) MICHAEL FARMWALD ET AL.	
	FILING DATE November 20, 2003	GROUP ART UNIT Unassigned 2111

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	FILING DATE November 20, 2003	GROUP ART UNIT Unassigned 211

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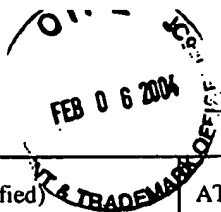
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GA	64.	4,206,833	04/27/93	Lee			
↓	65.	4,953,128	08/28/90	Kawai et al.			
↓	66.	5,140,688	08/18/92	White et al.			
↓	67.	5,018,111	05/21/91	Madland			
↓	68.	4,845,664	07/04/89	Aichelmann, Jr. et al.			
GA	69.	4,734,880	03/29/88	Collins			

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GA	70.	D.T. Wong et. al., "An 11-ns 8Kx18 CMOS Static RAM with 0.5-μm Devices", IEEE Journal of Solid State Circuits, vol. 23 No. 5, pp. 1095-1103 (Oct. 1988)
↓	71.	T. Williams et. al., "An Experimental 1-Mbit CMOS SRAM with Configurable Organization and Operation", IEEE Journal of Solid State Circuits, vol. 23 No. 5, pp. 1085-1094 (Oct. 1988)
↓	72.	D. Jones, "Synchronous static ram", Electronics and Wireless World, vol.93, no.1622, pp. 1243-4 (Dec. 87)
↓	73.	F. Miller et. al., "HIGH FREQUENCY SYSTEM OPERATION USING SYNCHRONOUS SRAMS", Midcon/87 Conference Record, pp. 430-432 Chicago, IL, USA; 15-17 Sept. 1987
↓	74.	K. Ohta, "A 1-Mbit DRAM with 33-MHz Serial I/O Ports", IEEE Journal of Solid State Circuits, vol. 21 No. 5, pp. 649-654 (Oct. 1986)
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↓	76.	F. Towler et. al., "A 128k 6.5ns Access/ 5ns Cycle CMOS ECL Static RAM", 1989 IEEE international Solid State Circuits Conference, (Feb. 1989)
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↓	78.	D. Wendell et. al. "A 3.5ns, 2Kx9 Self Timed SRAM", 1990 IEEE Symposium on VLSI Circuits (Feb 1990)
↓	79.	S. Watanabe et. al., "AN Experimental 16-Mbit CMOS DRAM Chip with a 100-MHz Serial READ/WRITE Mode", IEEE Journal of Solid State Circuits, vol. 24 No. 3, pp. 763-770 (June 1982)
↓	80.	K. Numata et. al. "New Nibbled-Page Architecture for High Density DRAM's", IEEE Journal of Solid State Circuits, vol. 24 No. 4, pp. 900-904 (Aug. 1989)
↓	81.	H. L. Kalter et al. "A 50-ns 16Mb DRAM with a 10-ns Data Rate and On-Chip ECC" IEEE Journal of Solid State Circuits, vol. 25 No. 5, pp. 1118-1128 (Oct 1990)
GA	82.	J. Sonntag et al. "A Monolithic CMOS 10MHz DPLL for Burst-Mode Data Retiming", IEEE International Solid State Circuits Conference (ISSCC) February 16, 1990

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	APPLICANT(S) MICHAEL FARMWALD ET AL.	
	FILING DATE November 20, 2003	GROUP ART UNIT Unassigned 2111

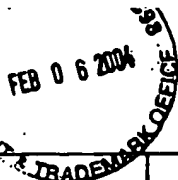
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↓	84.	4,860,198	08/22/89	Takenaka			
	85.	3,969,706	07/13/76	Proebsting et al.			
	86.	4,766,536	08/23/88	Wilson, Jr. et al.			
	87.	4,998,262	03/05/91	Wiggers			
	88.	4,757,473	07/12/88	Kurihara et al.			
	89.	4,792,926	12/20/88	Roberts			
	90.	4,811,202	03/07/89	Schabowski			
	91.	5,034,917	07/23/91	Bland et al.			
	92.	5,301,278	04/05/94	Bowater et al.			
	93.	5,153,856	10/06/92	Takahashi			
	94.	4,853,896	08/01/89	Yamaguchi			
↓	95.	4,747,079	05/24/88	Yamaguchi			
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GA	98.	M. Horowitz et. al., "MIPS-X: A 20-MIPS Peak 32-bit Microprocessor with On-Chip Cache", IEEE Journal of Solid State Circuits, vol. 22 No. 5, pp. 790-799 (Oct. 1987)
↓	99.	R. L. Schmidt, "A memory Control Chip for Formatting Data into Blocks Suitable for Video Coding Applications", IEEE Transactions on Circuits And Systems, vol. 36 No. 10, pp. 1275-1280 (Oct 1989)
↓	100.	L. R. Metzger, "A 16K CMOS PROM with Polysilicon Fusible Links", IEEE Journal of Solid State Circuits, vol. SC-18 No. 5, pp. 561-567 (Oct. 1983)
GA	101.	A. L. Yuen, "A 32K ASIC Synchronous RAM Using a Two-Transistor Basic Cell", IEEE Journal of Solid State Circuits, vol. 24 No. 1, pp. 57-61 (Oct. 1989)

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GA	102.	5,051,889	09/24/91	Fung et al.			
↓	103.	5,361,277	11/01/94	Grover			
↓	104.	4,954,987	09/04/90	Auvinen et al.			
↓	105.	4,570,220	02/11/86	Tetrick et al.			
↓	106.	4,247,817	01/27/81	Heller			
↓	107.	4,519,034	05/21/85	Smith et al.			
↓	108.	3,691,534	09/12/72	Varadi et al.			
↓	109.	4,920,486	04/24/90	Nielsen			
GA	110.	4,263,650	04/21/81	Bennett et al.			

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↓	112.	J. Peterson, "System-Level Concerns Set Performance Gains", High Performance Systems, pp. 71-77 (Sept. 89)
↓	113.	N. Margulis, "Single Chip CPU Eases Single Chip System Design", High Performance Systems, pp. 34-44 (Sept. 89)
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↓	115.	D.T. Wong, "An 11-ns 8Kx18 CMOS Static RAM with 0.5-um Devices", IEEE Journal of Solid State Circuits, vol. 23, No. 5, pp. 1095-1103 (Oct. 1988)
GA	116.	A. Agarwal et al., "An Evaluation of Directory Schemes for Cache Coherence", IEEE, pp. 280-289, 1988.

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GA	117.	4,719,602	01/12/88	Hag et al.			
↓	118.	5,023,488	06/11/91	Gunning			
	119.	4,754,433	06/28/88	Chin et al.			
	120.	3,771,145	11/06/73	Wiener			
	121.	5,021,985	06/04/91	Hu et al.			
	122.	4,821,226	04/11/89	Christopher et al.			
	123.	4,882,712	11/21/89	Ohno et. al.			
	124.	4,951,251	08/21/90	Yamaguchi et al.			
↓	125.	4,928,265	12/29/92	Beighe et al.			
GA	126.	5,107,465	04/21/92	Fung et al.			

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GA	127.	D. Hawley, "Superfast Bus Supports Sophisticated Transactions", High Performance Systems, pp. 90-94 (Sept. 89)
↓	128.	M. Bazes, "A Programmable NMOS DRAM Controller for Microcomputer Systems with Dual Port Memory and Error Checking and Correction", IEEE Journal of Solid State Circuits, vol. SC-18, No. 2, pp. 164-172 (April 1983)
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GA	134.	4,633,735	05/05/87	Novak, et. al			
	135.	5,684,753	11/04/97	Hashimoto, et al			
	136.	4,322,635	03/30/81	Redwine			
	137.	4,916,670	04/10/90	Suzuki et al.			
	138.	5,006,982	04/09/91	Ebersole et al.			
	139.	4,636,986	01/13/87	Pinkham			
	140.	4,979,145	12/18/90	Remington et al.			
	141.	5,276,846	01/04/94	Aichelmann Jr., et. al			
	142.	4,761,567	Aug. 2, 1988	Walters, Jr. et al.			
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GA	151.	4,482,999	11/13/84	Janson et al.			
↓	152.	5,029,124	07/02/91	Leahy et al.			
↓	153.	5,193,193	03/09/93	Iyer			
↓	154.	4,926,385	05/15/90	Fujishima et al.			
↓	155.	4,566,099	01/21/86	Magerl			
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↓	157.	4,589,108	05/13/86	Billy			
↓	158.	4,870,622	09/26/89	Aria et al.			
↓	159.	4,878,166	10/31/89	Johnson et al.			
↓	160.	4,849,965	07/18/89	Chomel et al.			
GA	161.	4,851,990	07/25/89	Johnson et al.			

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GA	162.	sho 58-31637A	Feb 24, 1983	Japan			
↓	163.	sho 59-165285A	Mar. 11, 1983	Japan			
↓	164.	sho 60-261095A	June 6, 1984	Japan			
↓	165.	sho 63-300310	Dec. 7, 1988	Japan			
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↓	167.	sho 58-184626A	Oct 28, 1983	Japan			
GA	168.	0 276 871	03/08/88	EPO			

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GA	169.	4,528,661	07/09/85	Bahr et al.			
↓	170.	4,048,673	09/13/77	Hendrie et al.			
↓	171.	4,748,617	05/31/88	Drewlo			
↓	172.	4,839,801	06/13/89	Nicely et al.			
↓	173.	4,949,301	08/14/90	Joshi et al.			
↓	174.	3,950,735	04/13/76	Patel			
↓	175.	4,047,246	09/06/77	Kerllenevich et al.			
↓	176.	4,763,249	08/09/88	Bomba et al.			
GA	177.	4,625,307	11/25/86	Tulpule et al.			

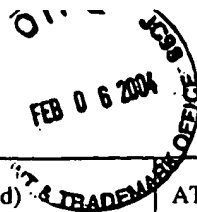
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GA	178.	SHO 58-192154	Nov. 9, 1983	Japan			
↓	179.	SHO 63-34795	Feb. 15, 1988	Japan			
↓	180.	SHO 61-107453	May 26, 1986	Japan			
↓	181.	SHO 63-91766	April 22, 1988	Japan			
↓	182.	SHO 62-16289	Jan. 24, 1987	Japan			
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↓	187.	3,771,145	11/06/73	Wiener			
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↓	189.	4,629,909	12/16/86	Cameron			
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↓	192.	4,499,536	02/12/85	Gemma et al.			
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↓	197.	Lineback, J. Robert, "System Snags Shouldn't Slow the Boom in Fast Static RAMs," Electronics, pp. 60-62 (July 23, 1997)
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	203.	4,845,677	07/04/89	Chappell, et. al			
	204.	4,873,671	10/10/89	Kowshik, et. al			
	205.	4,876,670	10/24/89	Nakabayashi, et. al			
	206.	5,179,667	01/12/199 3	Iyer			
	207.	4,901,036	02/13/90	Herold, et. al			
	208.	4,970,418	11/13/90	Masterson			
	209.	5,210,715	05/11/93	Houston			
	210.	4,928,265	05/22/90	Higuchi et al.			
	211.	4,953,130	08/28/90	Houston			
	212.	5,251,309	10/05/93	Kinoshita et al.			
GA	213.	4,099,231	07/01/78	Kotok et al.			

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GA	216.	Sho 62-71428	03/27/87	JP			
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↓	221.	5,016,226	05/14/91	Hiwada, et. al		
	222.	5,023,835	06/11/91	Akimoto, et. al		
	223.	5,036,495	07/30/91	Busch, et. al		
	224.	5,111,486	05/05/92	Oliboni, et. al		
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PTO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	PATENT NO. DOCKET NO. 57941.000063	SERIAL NUMBER 10/716,596
APPLICANT(S) MICHAEL FARMWALD ET AL.		
FILING DATE November 20, 2003		GROUP ART UNIT Unassigned 2111

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↓	252.	4,253,147	Feb. 24, 1981	MacDougall et al.		
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PTO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	PATENT NO. 57941.000063	SERIAL NUMBER 10/716,596
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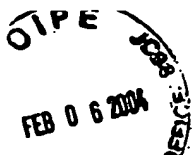
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GA	276.	61028248	Feb. 7, 1986	Japan			
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	APPLICANT(S) MICHAEL FARMWALD ET AL.	
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